

CMOS : 400

STANDARD; Set time, date, disable any IDE drives FLOPPY = a: 1.44

ADVANCED; **Boot Virus** = disabled

Cpu level 1 cache level 1 & 2 = enabled

Cpu level 2 cache ECC = check cpu to see if chip is error correcting or not chip will have an E after the # eg 80522PX400512E

Bios update = enabled

Quick power self test = enabled

HDD sequence = scsi

Boot sequence = a-c

Boot up floppy seek = enabled

Floppy swap = disabled

Floppy access =R/W

IDE HDD = disabled

Security option = system

Ps/2 mouse = auto

Pci/vga = disabled

Os/2 onboard mem 64 = disabled

Video rom = disabled

C8000 – dffff = disabled

Boot up numlock = on

Typematic rate setting = enabled

Typematic rate = 6

Typematic delay = 250

CHIPSET

Sdram = by spd or chip speed

Sdram cas = default

Sdram ras to cas = default

Sdram ras precharge = default

Dram idle = default

Sdram ma = normal

Snoop ahead = default

Host bus = default

Graphics aperture = default

Video mem = default

PCI 2.1 support = enabled

Memory hole = disabled

Data integrity = non-ECC

Onboard FDC controller = enabled

Onboard floppy swap = no swap

Onboard serial port 1&2 = default

Parallel port = default

Uart2 = disabled

Onboard PCI enable = both

Ide ultra mode = disable

Ide 0 master, slave = auto

POWER MANAGEMENT

Power management = disable

PNP PCI SETUP

Pnp OS installed = no

Slot 1 irq = auto

PCI latency = 32 PCI clock

IRQ xx used by isa = no/ICU

DMA x used by ISA = no/ICU

ISA mem = no/ICU

Symbios scsi bios = disabled [unless using onboard scsi]

USB IRQ = disabled

